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FIG. 1

ELEMENT ID	LENGTH	DTIM COUNT	DTIM PERIOD	BITMAP CONTROL	PARTIAL VIRTUAL BITMAP
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FIG. 2

ELEMENT ID	LENGTH	ATIM WINDOW
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FIG. 3

FRAME DURATION DA	SA BSSID	SEQUENCE CONTROL FCS
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FIG. 4

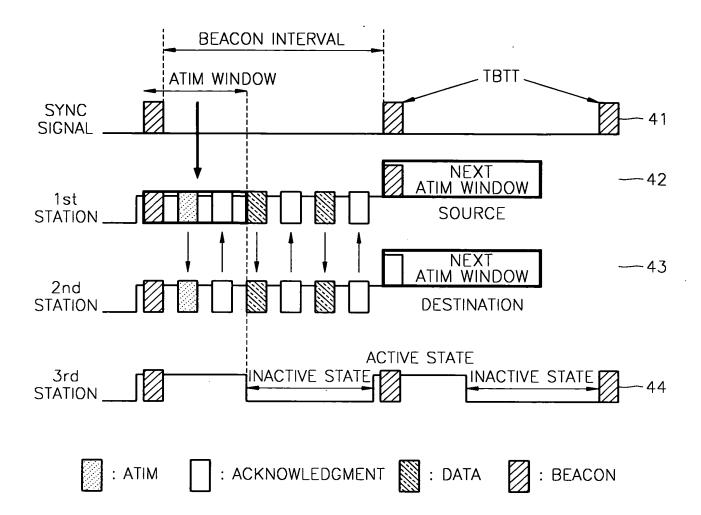


FIG. 5

MODE	MODULTION	CODE RATE	DATE RATE	BPS
1	BPSK	1/2	6Mbps	24
2	BPSK	3/4	9Mbps	36
3	BPSK	1/2	12Mbps	48
4	BPSK	3/4	18Mbps	54
5	16-QAM	1/2	24Mbps	96
6	16-QAM	3/4	36Mbps	144
7	64-QAM	2/3	48Mbps	192
8 ·	64-QAM	3/4	54Mbps	216

FIG. 6

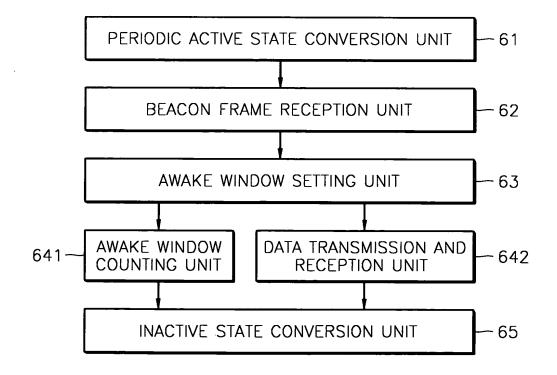


FIG. 7

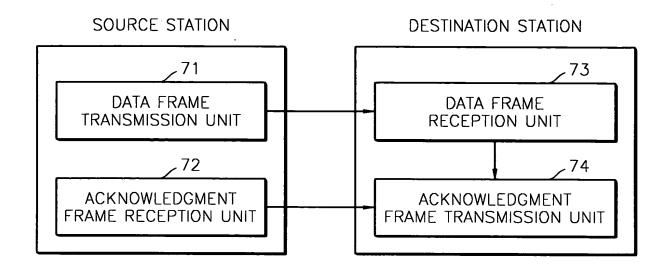
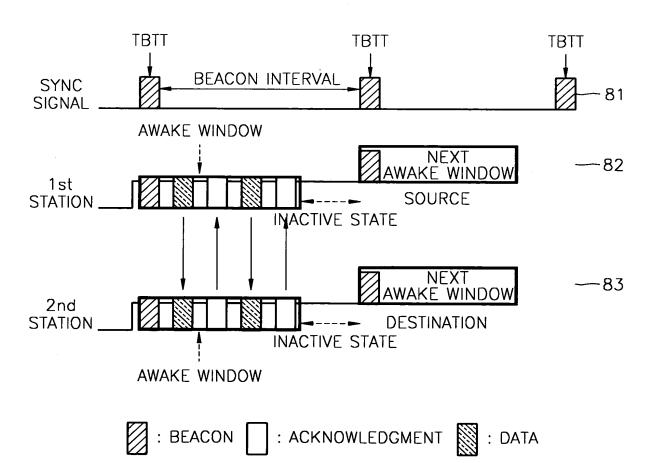
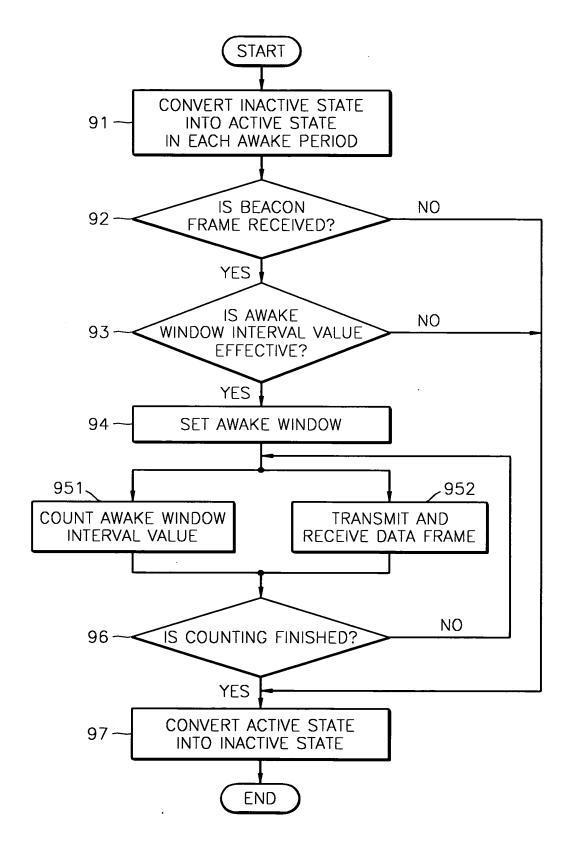


FIG. 8



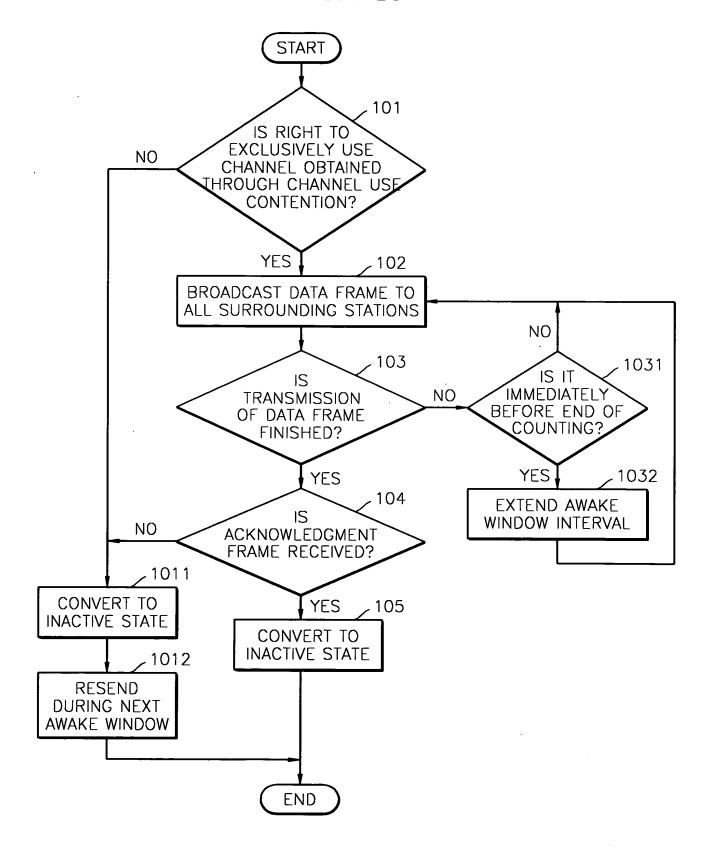
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FIG. 9



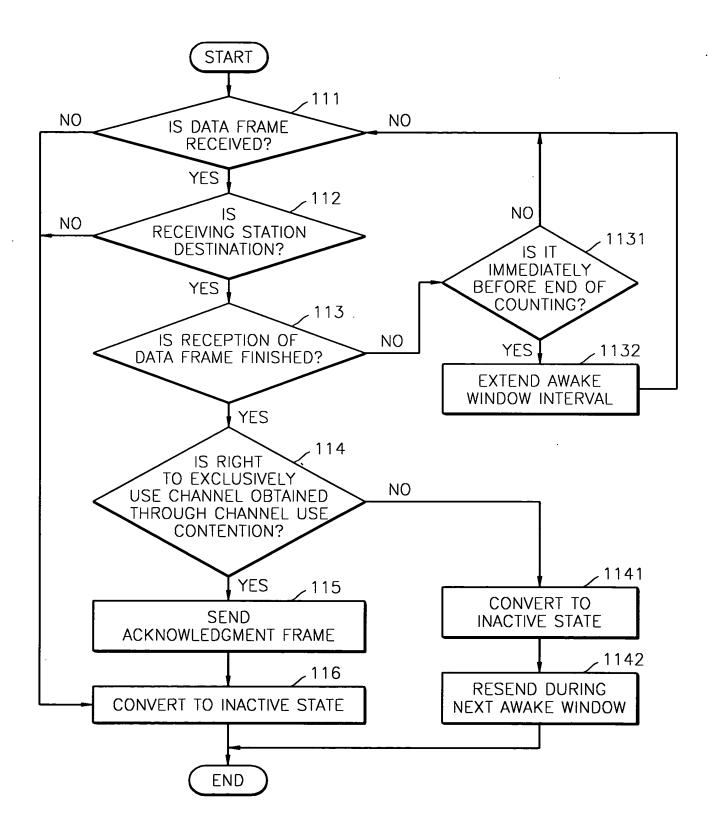
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FIG. 10



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FIG. 11



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FIG. 12

TBTT	TBTT	TBTT
SYNC BEACON INTERVAL		121
ATIM WINDOW DATA TRANSMIS INTERVAL PRIOR ART	NEXT ATIM WINDOW	—122
ACTIVE S	STATE	
PRESENT INVENTION INAC	NEXT AWAKE WINDOW TIVE STATE	—123
ACTIVE STATE		
: BEACON : ATIM] : ACKNOWLEDGMENT [:	DATA

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FIG. 13

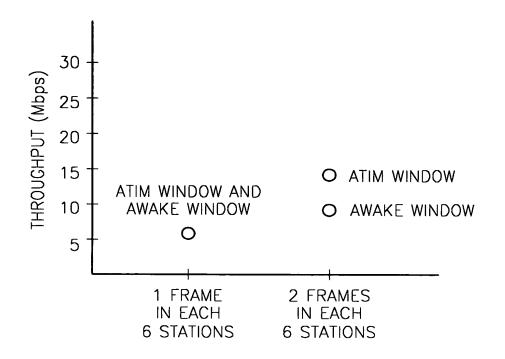


FIG. 14

